Substitute for Form 1449/PTO						Complete if Known				
INFORMATION DISCLOSURE						Application Number		10/810,093		
						Filing Date		March 26, 2004		
STATEMENT BY APPLICANT						First Named Inventor	r:	Khellah et al.		
(use as many sheets as necessary)						Art Unit		2824		
						Examiner Name		Andrew Q. Tran		
Sheet	45		of	1		Attorney Docket Num	nber	42P16596		
E.	. E.	NON PATENT LITERATURE DOCUMENTS								
Examiner Initials	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published								
at	1.	WANN, C. et al., "CMOS with Active Well Bias for Low-Power and RF/Analog Applications", IEEE, Symposium on VLSI Technology Digest of Technical Papers, 2000, pages 158-159.								
at	2.	BHAVNAGARWALA, A. J. et al., "The Impact of Intrinsic Device Fluctuations on CMOS SRAM Cell Stability", IEEE Journal of Solid-State Circuits, April 2001, pages 658-665, vol. 36, No. 4.								
at	3.				J. et al., "l 0, pages 3	Dynamic-Threshold (359-363.	CMOS SR	AM Cells for	Fast, Portable	
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Examiner Signature	Cul	ر م						Date Considered	04/25/2	200

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is attached.

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